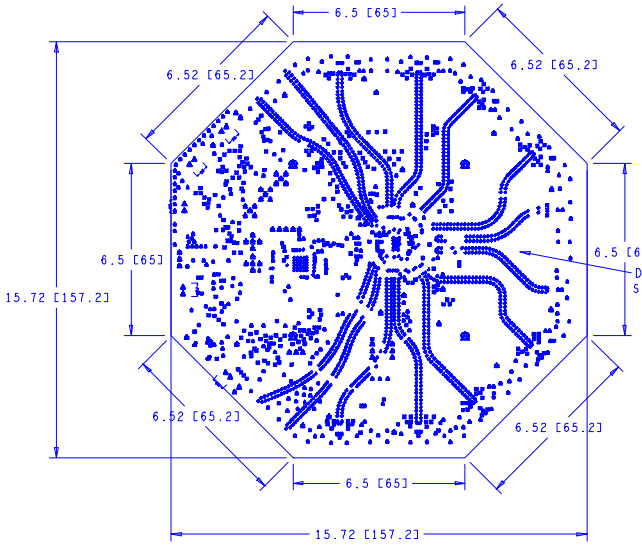
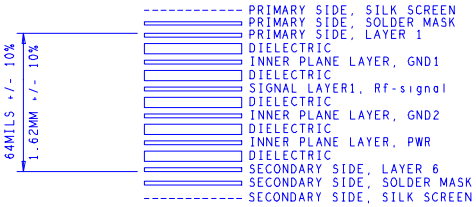
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	INITIAL RELEASE	12/24/14	TAVA

NOTES: UNLESS OTHERWISE SPECIFIED.

1. FABRICATE PER IPC-6012A CLASS 2.
2. MATERIAL:.
   DIELECTRIC: FR4 PER IPC-4101 WITH MIN TG 180A DEGREE C.
   COPPER: AS PER STACKUP.
   U.L. RATING: 94V-0 MINIMUM
3. SURFACE FINISH: ENIG
4. SOLDER MASK MATERIAL SHALL MEET ALL THE REQUIREMENTS OF IPC-SM-840C AND SHALL BE GREEN IN COLOR AND APPLIED OVER BARE COPPER.
5. SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING WHITE NON-CONDUCTIVE EPOXY INK.
6. 100% CONTINUITY TESTING USING DATABASE NETLIST SHALL BE PERFORMED. VENDOR TO IDENTIFY TEST PASSED IN SECONDARY SIDE.
7. VENDOR TO MARK DATE CODE AND LOGO IN ETCH OR IN LEGEND.
8. BOW AND TWIST SHALL NOT EXCEED 0.7% OF LONGEST SIDE .
9. DIELECTRICS AND LINEWIDTHS MAY BE ADJUSTED TO MEET THE IMPEDANCE REQUIREMENTS.
10. MINIMUM CONDUCTOR WIDTH: 4 MILS
    MINIMUM SPACING: 5 MILS
11. REQUEST FABRICATOR TO ADD TEAR DROPS IF NOT ADDED IN THE GERBERS.
12. MASK PADS HAS BEEN INCREASED BY 3MIL WITH RESPECT TO OUTER LAYERS



LAYER STACK-UP



REFER DOCUMENT("sienna-10oct2k14-13745") FOR COMPLETE STACK INFORMATION

SINGLE ENDED

LAYER NO.	CONDUCTOR WIDTH	REFERENCE LAYER	IMPEDANCE +/-10%
LAYER 1	5.35 mil	2	50 OHM +/-10%
LAYER 3	6.47 mil	2 & 4	50 OHM +/-10%
LAYER 6	N/A	N/A	N/A

DIFFERENTIAL PAIR

LAYER NO.	CONDUCTOR WIDTH	SPACING	REFERENCE LAYER	IMPEDANCE +/-10%
1	N/A	N/A	N/A	N/A
3	5.00 mil	5.75 mil	2 & 4	90 OHMS+/-10%
6	5.00 mil	6.53 mil	5	90 OHMS+/-10%

DIFFERENTIAL PAIR

LAYER NO.	CONDUCTOR WIDTH	SPACING	REFERENCE LAYER	IMPEDANCE +/-10%
1	4.00 mil	7.26 mil	2	100 OHMS+/-10%
3	4.00 mil	7.26 mil	2 & 4	100 OHMS+/-10%
6	N/A	N/A	N/A	N/A

DRILL CHART: TOP to BOTTOM
ALL UNITS ARE IN MILS

FIGURE	SIZE	TOLERANCE	PLATED	QTY
◆	8.0	+3.0/-3.0	PLATED	1287
■	12.01	+3.0/-3.0	PLATED	409
●	20.0	+3.0/-3.0	PLATED	283
◆	27.99	+3.0/-3.0	PLATED	5
●	35.98	+3.0/-3.0	PLATED	16
◆	40.0	+3.0/-3.0	PLATED	51
▲	41.34	+3.0/-3.0	PLATED	10
⊗	117.99	+2.0/-2.0	NON-PLATED	4
◤	74.8x27.6	+3.0/-3.0	PLATED	2

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON: ANGLES +/- 2°
2 PL DECIMALS +/- .010
3 PL DECIMALS +/- .005
THIRD ANGLE PROJECTIONS

CYPRESS PROPRIETARY
THIS DOCUMENT CONTAINS CONFIDENTIAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHIRIZED BY CYPRESS.

SIGNATURES		DATE
DRAWN	ECAD	12/24/14
CHECKED	TAVA	12/24/14
ENGRG	TAVA	12/24/14
ISSUED		

CYPRESS PERFORM
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

FABRICATION DRAWING
CY3679 EVALUATION KIT

SIZE	FSCM NO	DWG NO	REV
C		610-60200-01	01

SCALE: 1/1 SHEET 1 OF 1